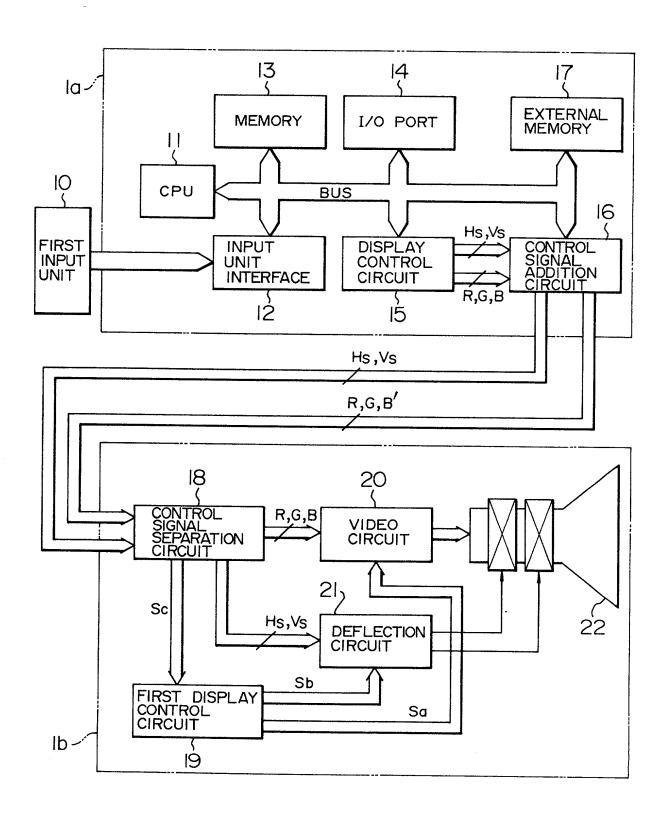
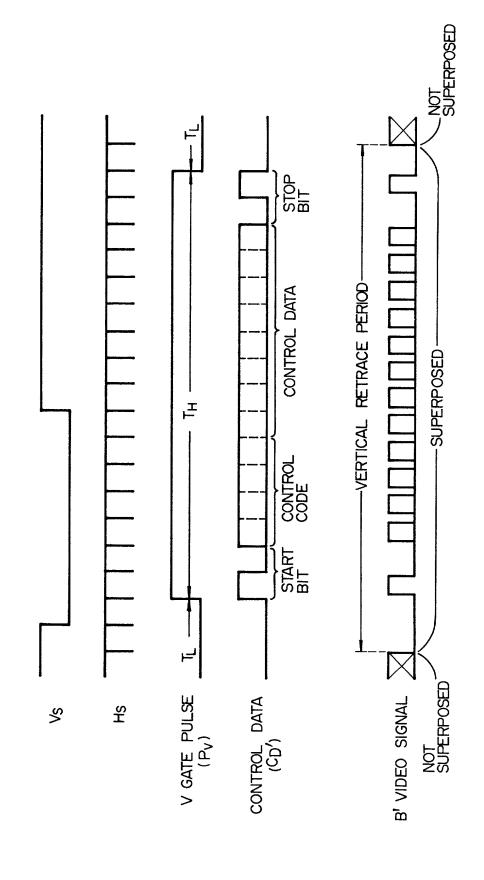
FIG. I

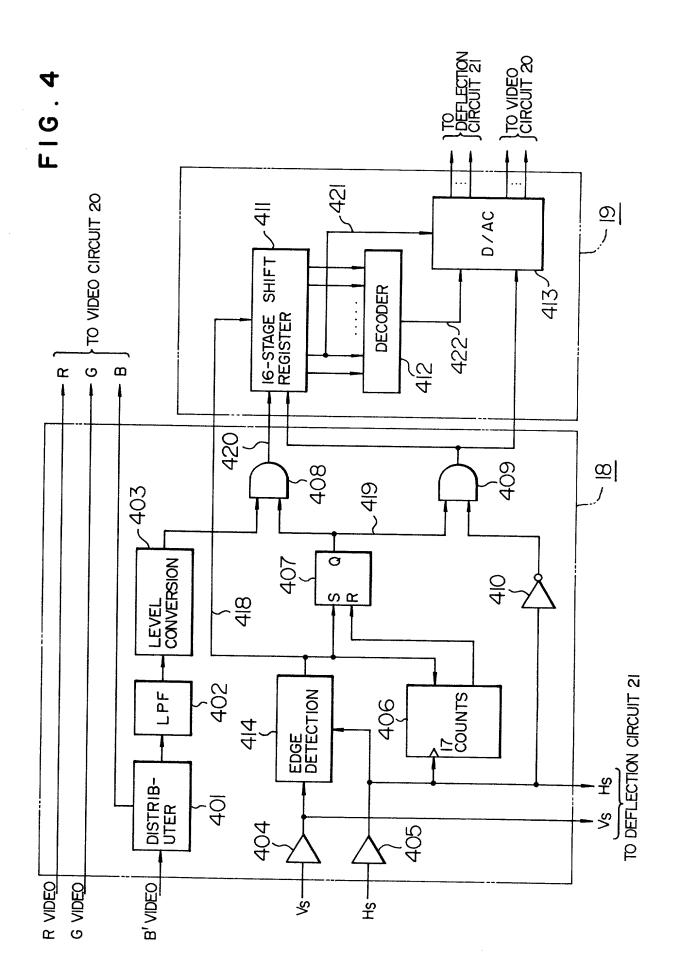


6,R 9 -\s -HS , B SWITCH 167 LEVEL CONVERSION 166 165 \sim 162 . ල පි) DATA LATCH SHIFT REGISTER ₽, O 64 69 S œ σ₀′ Sca 17 COUNTS <u>168</u> ADDRESS DECODER EDGE DETECTION 163 G AND -R VIDEO B VIDEO £ \$

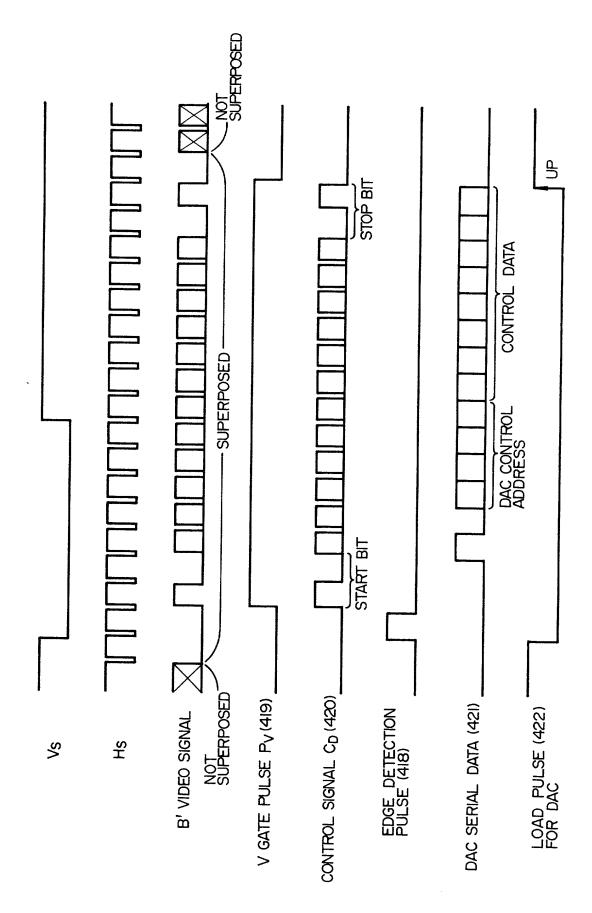
F16.2

FIG. 3





F1G. 5



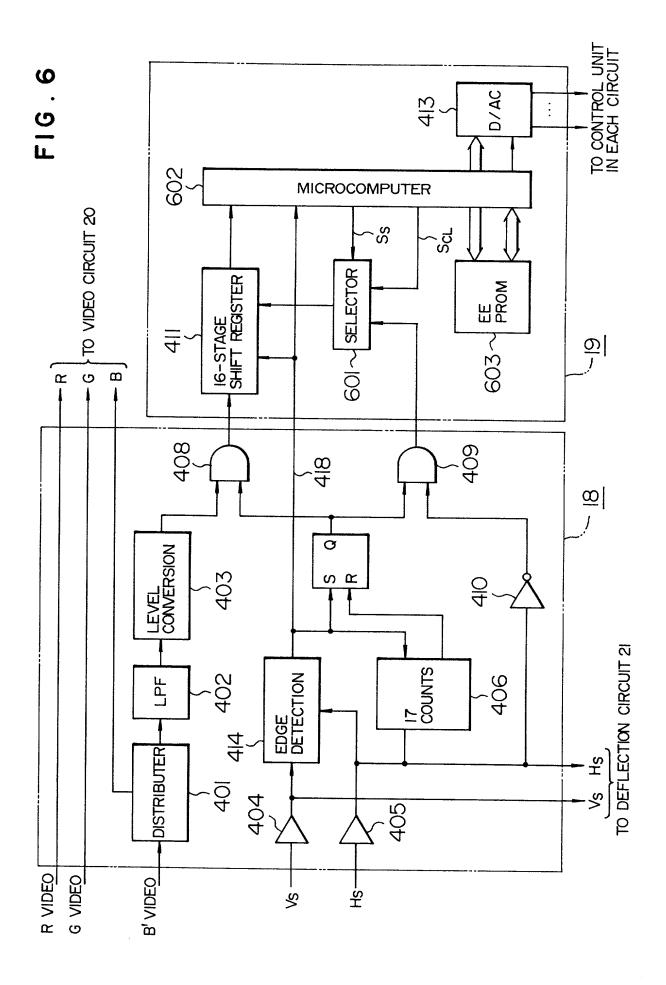


FIG. 7

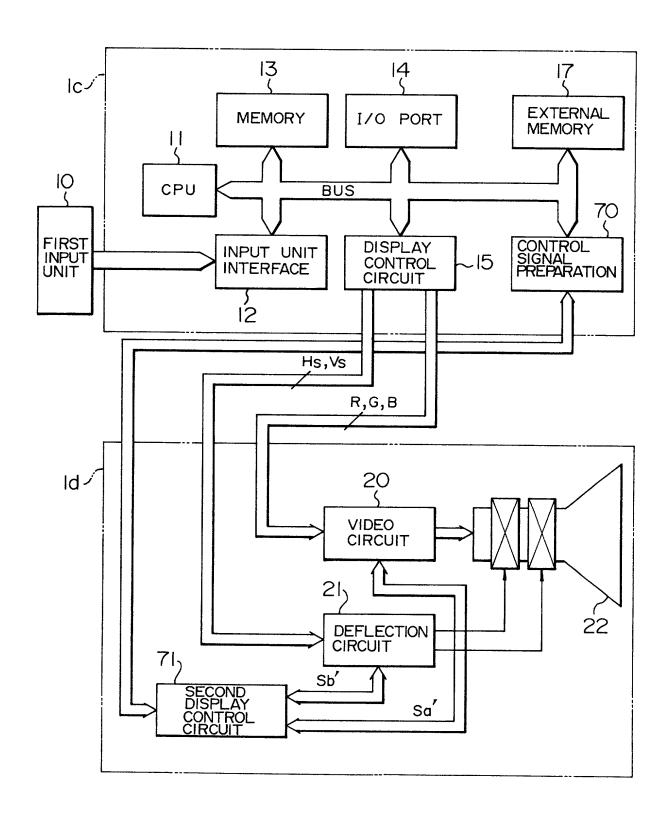
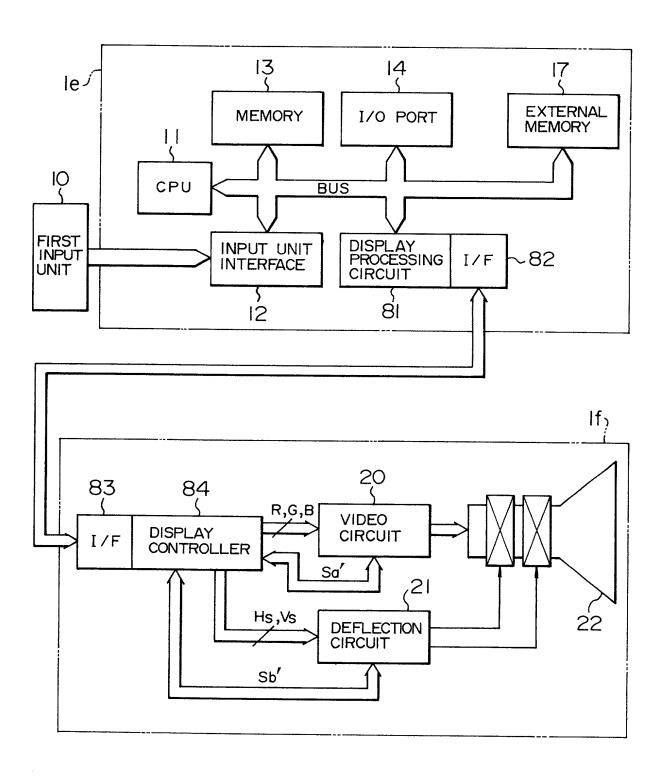


FIG.8



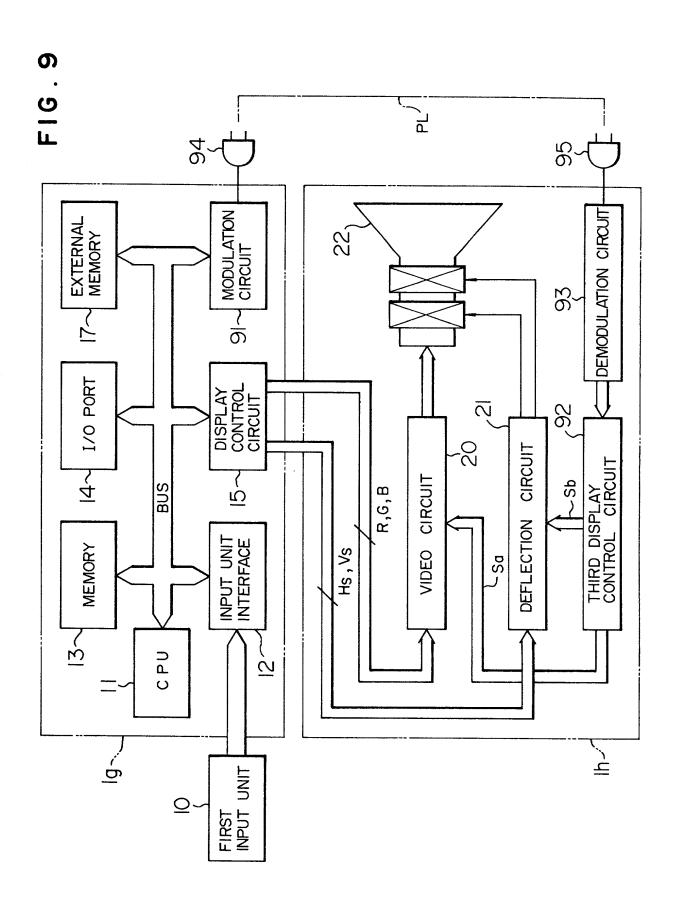


FIG. 10

